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1. **Code compression for embedded VLIW processors using variable-to-fixed coding**
Yuan Xie; Wolf, W.; Lekatsas, H.;
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2. **A code decompression architecture for VLIW processors**
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6. **LZW-based code compression for VLIW embedded systems**
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7. **Dictionary-based program compression on TTAs: effects on area and power consumption**
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8. **Making compaction-based parallelization affordable**
Nakatani, T.; Ebcioglu, K.;
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9. **A single mediaprocessor-based programmable ultrasound system**
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10. **Performance comparison of two-dimensional discrete wavelet transform computation schec digital signal processor**
Masselos, K.; Andreopoulos, Y.; Stouraitis, T.;
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11. **An instruction set architecture based code compression scheme for embedded processors**
Menon, S.K.; Shankar, P.;
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Digital Object Identifier 10.1109/DCC.2005.15
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L42	L41 and I15	15	L42
L41	(vliw$1 or very near1 (large or long)) near20 (compress$5 or decompress$5) and (mask$5) near35 (null or nop or "no" near1 operation or mask$5) (vliw$1 or very near1 (large or long)) near35 (mask$5) near35 (null or	108	L41

L40	nop or "no" near1 operation or mask$5) near35 (compress$5 or decompress$5)	10	L40
L39	l31 and l19	2	L39
L38	5669001.pn.	2	L38
L37	L36 and l15	28	L37
L36	L32 and (length or width or size)	570	L36
L35	L32 near55 (length or width or size)	65	L35
L34	L33 not l6	65	L34
L33	L32 near35 (length or width or size)	65	L33
L32	(vliw$1 or very near1 (large or long)) near35 (mask$5) near35 (null or nop or "no" near1 operation or mask$5)	680	L32
L31	(vliw or very near1 (large or long)) near35 (mask$5) near35 (null or nop or "no" near1 operation or mask$5)	680	L31
DB=PGPB,USPT; PLUR=YES; OP=OR			
L30	l25 not l26	12	L30
L29	l12 and l23	1	L29
L28	l12 and l21	0	L28
L27	l12 and l19	4	L27
L26	l12 and l17	28	L26
L25	l12 and l15	40	L25
L24	(710/66-68)[CCLS]	602	L24
L23	(710/66-68)![CCLS]	602	L23
L22	(717/141-143)[CCLS]	721	L22
L21	(717/141-143)[CCLS]	721	L21
L20	(717/106-167)![CCLS]	9592	L20
L19	(717/106-167)[CCLS]	9592	L19
L18	(712/24,225,227,300)[CCLS]	1872	L18
L17	(712/24,225,227,300)[CCLS]	1872	L17
L16	(712/2-300)[CCLS]	12943	L16
L15	(712/2-300)![CCLS]	12943	L15
DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			
L14	L13 not l6	79	L14

L13	L12 not l11	85	L13
L12	(vliw or very near1 (large or long)) near35 (reduc$5 or compres$5 or compact$4 or decompress$5) near45 (null or nop or "no" near1 operation or mask$5)	106	L12
L11	(vliw or very near1 (large or long)) near55 (reduc$5 or compres$5 or compact$4 or decompress$5) near6 (length or width or size) near25 (null or nop or "no" near1 operation or mask$5)	21	L11
L10	(vliw or very near1 (large or long)) and (reduc$5 or compres$5 or compact$4 or decompress$5) near6 (length or width or size) near25 (null or nop or "no" near1 operation or mask$5)	508	L10
L9	L6 not (l1 or l3)	68	L9
L8	L6 not l1	70	L8
L7	L6 not l3	68	L7
L6	(vliw or very near1 (large or long)) and (compres$5 or compact$4 or decompress$5) near6 (length or width or size) near25 (null or nop or "no" near1 operation or mask$5)	77	L6
L5	(vliw or very near1 (large or long)) and (compres$5 or compact$4 or decompress$5) near6 (length or width or size) near45 (null or nop or "no" near1 operation or mask$5)	77	L5
L4	L3 not l1	3	L4
L3	(vliw or very near1 (large or long)) near45 (compres$5 or compact$4 or decompress$5) near6 (length or width or size) near45 (null or nop or "no" near1 operation or mask$5)	10	L3
L2	6704859.pn.	3	L2
L1	(vliw or very near1 (large or long)) near25 (compres$5 or compact$4 or decompress$5) near4 (length or width or size) near25 (null or nop or "no" near1 operation)	7	L1

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